

## **ABSTRACT OF THE DISCLOSURE**

A system may include multiple nodes, and each node may include a processing subsystem and an interface that are coupled by an address network and a data network.

- 5 The nodes' interfaces may communicate over an inter-node network. Each processing subsystem may transition an access right to a coherency unit in response to a data packet on the data network and transition an ownership responsibility for the coherency unit in response to an address packet on the address network such that the access right transitions at a different time than the ownership responsibility transitions. An interface within a
- 10 node may be configured to delay providing a data packet on the node's data network until the interface receives an indication that shared copies of the coherency unit in other nodes have been invalidated.